

Compal Confidential

LA-A141P Schematics Document

Intel Clover View

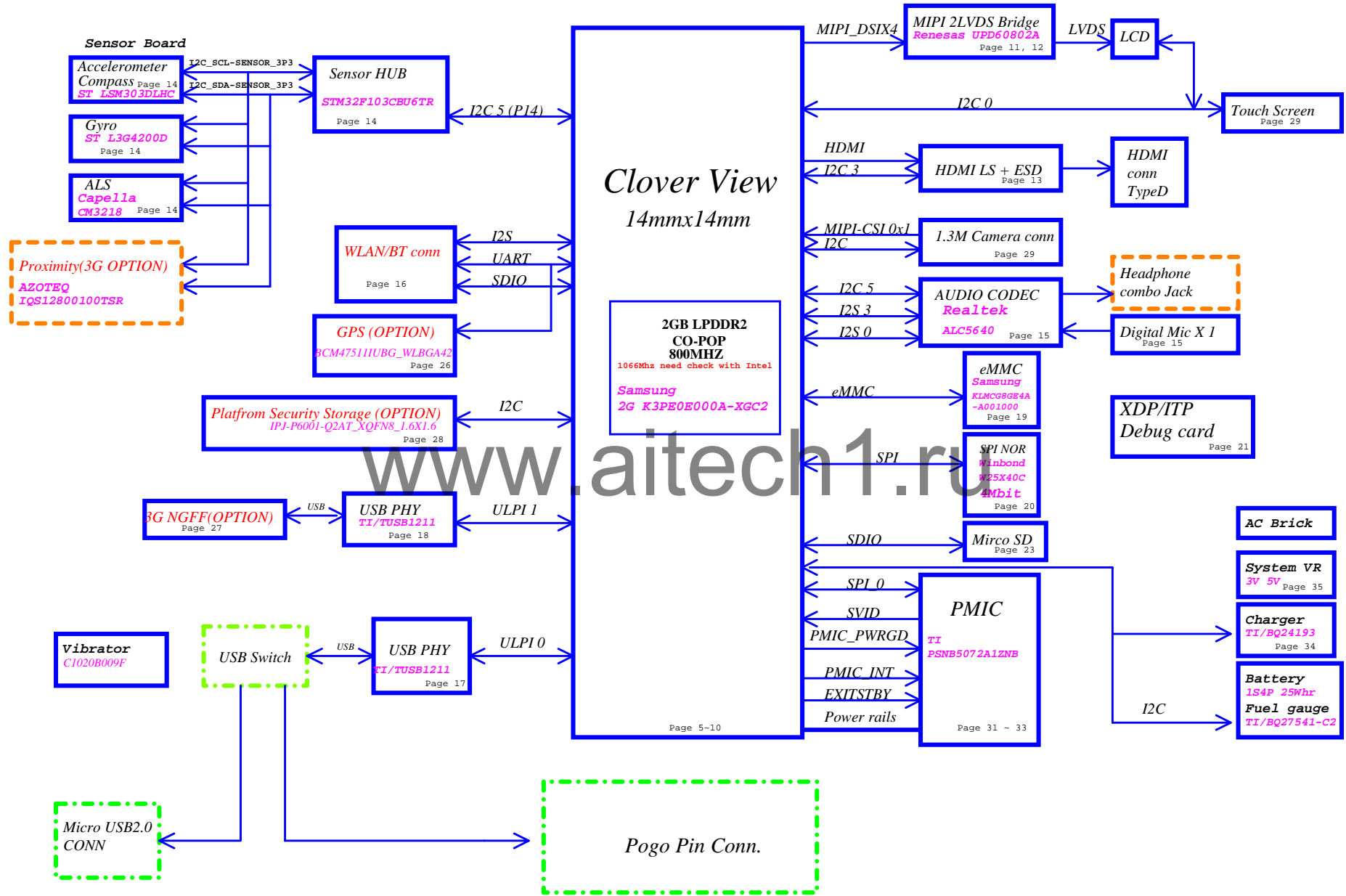
2013-03-25

REV: 0.3

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System Block Diagram



Docking daughter board

Audio daughter board

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Power rail

	Net name	Voltage	Comment
	DC_IN	5V or 12V	adapter input
	+V_BATTERY	Max 4.2V	Battery input
	+VBATA	3.4V~4.2V	It's tablet's "B+"
	+V5A	5V	Should be 5VALW
	+V3.3A	3.3V	Should be 3VALW
	+V5S	5V	Should be 5VS
	+V3.3S	3.3V	Should be 3VS
	+V_VCC	0.3~1.2V	0.95V is default value
	+V_VNN	0.6~1.2V	0.95V is default value
	+V_VNNAON	0.6~1.2V	0.95V is default value
PMIC BUCK Converter	+V_LP22_VCCAON	1.250V	1.25V is default value
	+V_LP80_AON	1.836V	1.836V is default value
	+V_LP08_VCCAON	1.08V	
	+V_LP08_VCCAS	1.08V	
PMIC LDO	+V_LP08_VCC	1.08V	
	+V_LP00_VCCAS	1.025V	
	+V_LP00_VCCA	1.025V	
	+V_2P85_LP80_VCCSDIO	2.85V	2.85V is default value
	+V_2P80_VPROG1	1.2V	1.2V is default value
	+V_2P80_VPROG2	1.2V	1.2V is default value
	+V_2P85_EM0MC1	2.85V	
	+V_2P85_EM0MC2	2.85V	No routing
	+V_LP80_AON	1.8V	
	+V_LP80_VCCAON	1.8V	
PMIC PWM output	+V_LP80_VCC	1.8V	
	+V_3P30_VCC	3.3V	

IO MAP

Interface	Device
MIPI DSI	LVDS Bridger
MIPI CSI	X4 NC
	X1 1.3M Camera
MIPI HSI	NC
LPC	NC
EMMC	0 EMMC
	1 NC
HDMI	HDMI Conn.
SDIO	0 Micro SD
	1 WIFI/BT
	2 NC
SVID	PMIC
SPI	0 FLASH ROM/PMIC/XDP
	1 XDP
	2 Test Point
	3 NC
ULPI	0 External USB/POGO
	1 USB 3G (Reserve)
UART	0 WIFI/BT
	1 GPS
	2 XDP
I2S	0 Codec
	1 BT PCM
	2 NC
	3 Codec
I2C	0 Panel / Touch screen
	1 NC
	2 Battery/PSS
	3 HDMI
	4 1.3M camera
	5 Codec / Sensor Hub

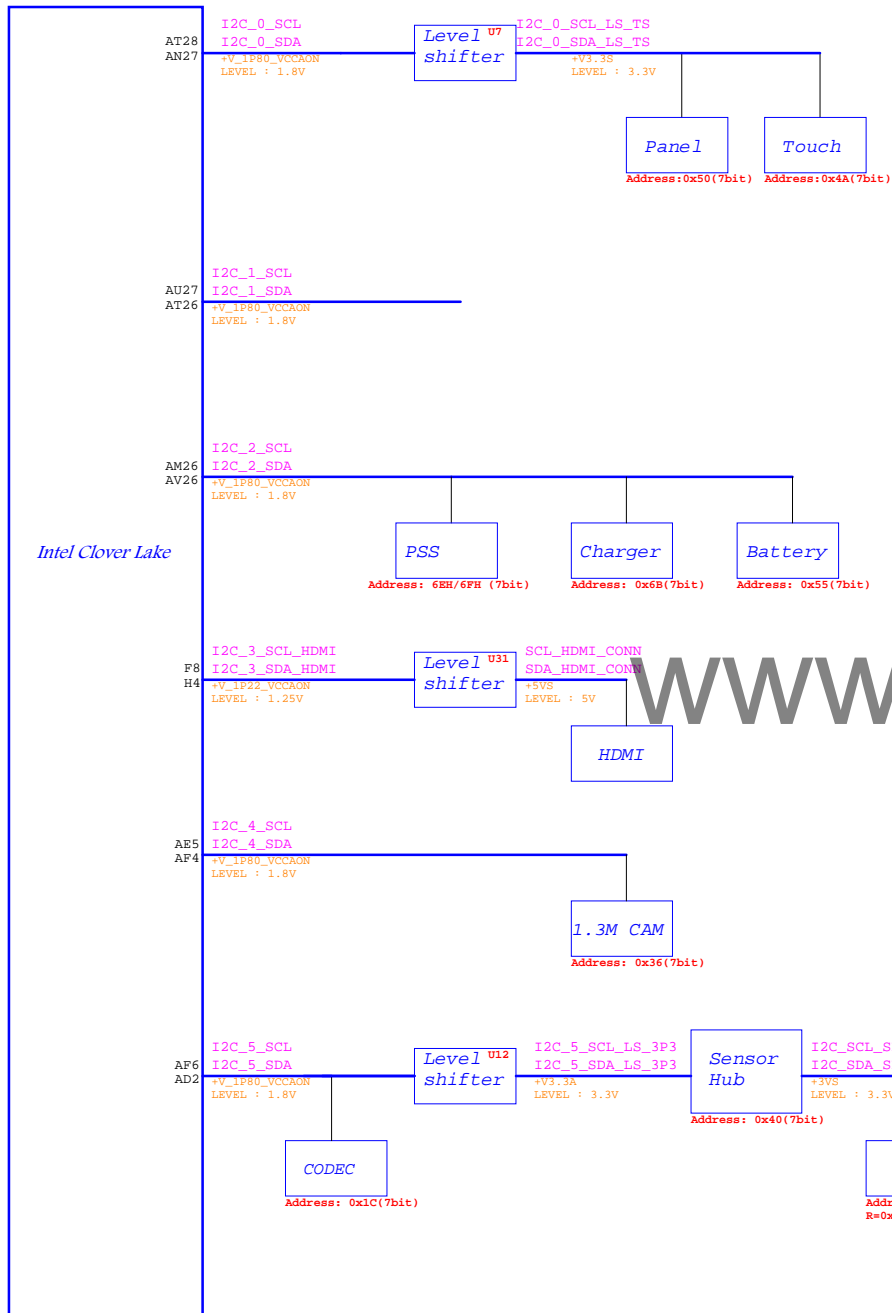
BOM Structure

ME@	ME usage
3G@	for 3G/LTE reservation
XDP@	for XDP function
NO_XDP@	for XDP function disable
SAMMEM@	for SAMSUNG DRAM
HYNNMEM@	for HYNIX DRAM
M@	for EMI/ESD/RF Request
SAM64@	for SAMSUNG 64G eMMC
SAM32@	for SAMSUNG 32G eMMC
SDK64@	for Sandisk 64G eMMC
SDK32@	for Sandisk 32G eMMC
GPS@	For GPS SKU
PSS@	For PPS SKU

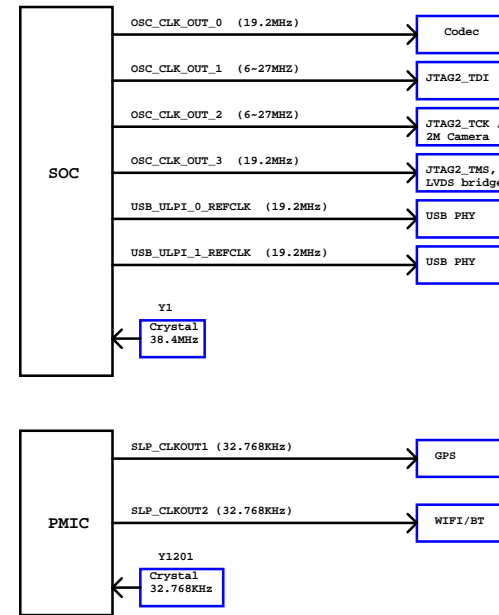
RF@ RF request to reseve

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I2C Routing

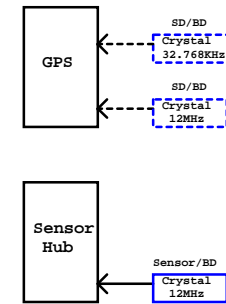


Clock Distribution



MB

SUB



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2012/09/20				2014/09/20				I2C ROUTING/Clock Distribution			
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Must Disable internal pull-high for uSD interface !!

11/28 EMI request to add R332

MicroSD

WIFI/BT

11/22 BT without RESET request

ULPI USB 0 (external USB)

U1C

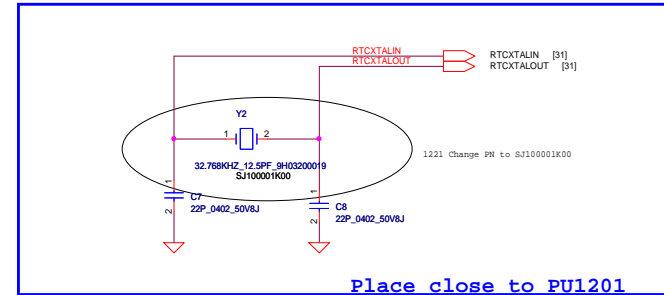
EMMC

ULPI USB 1 (3G)

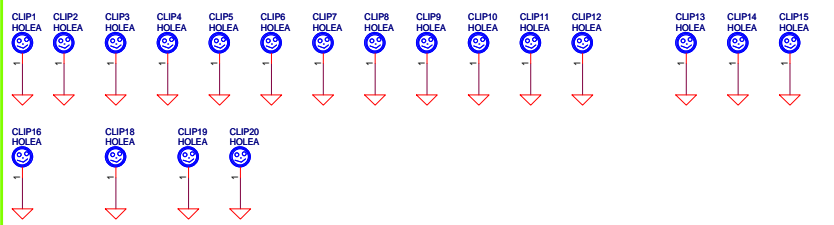
1204 Follow Intel's recommend. Change to 22 ohm

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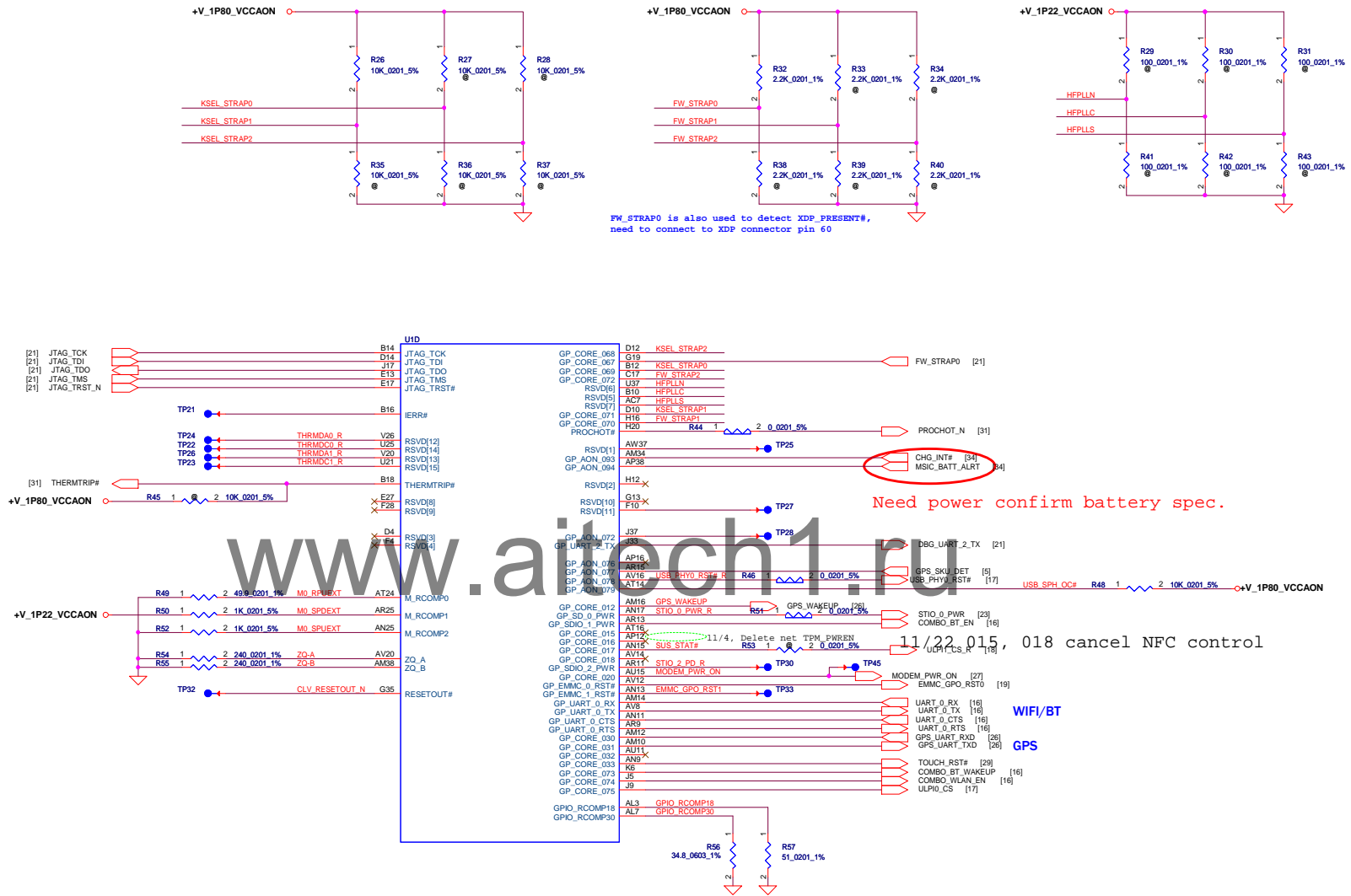
Those two pins were labeled in error on the SoC and in the EDS.
The correct pinout is:
ULPI_1_DIR = AU37
ULPI_1_STP = AR33

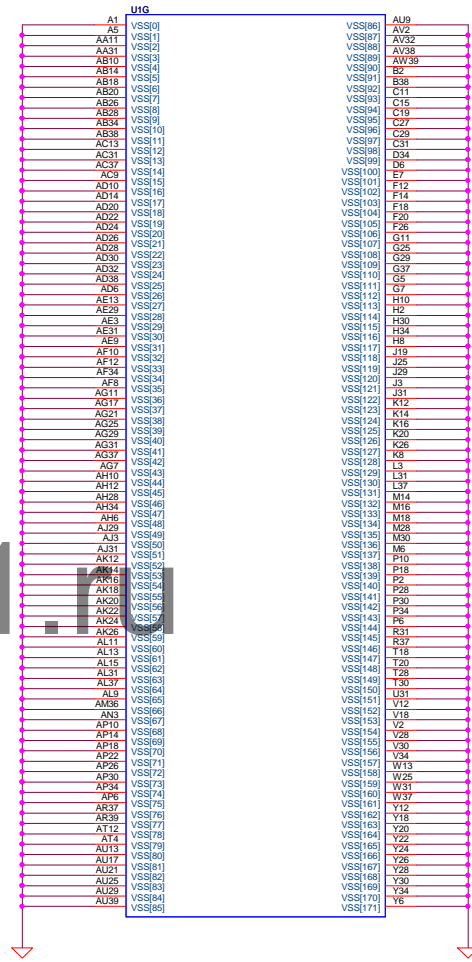
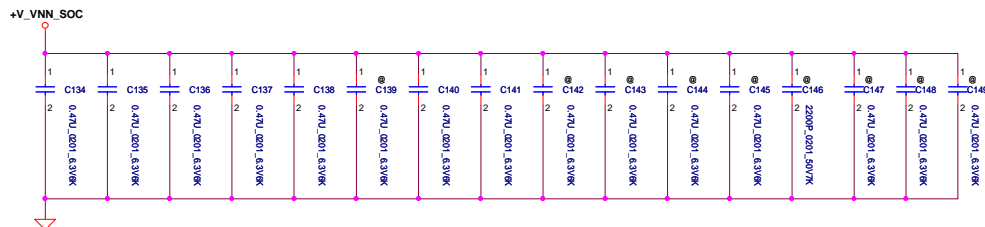
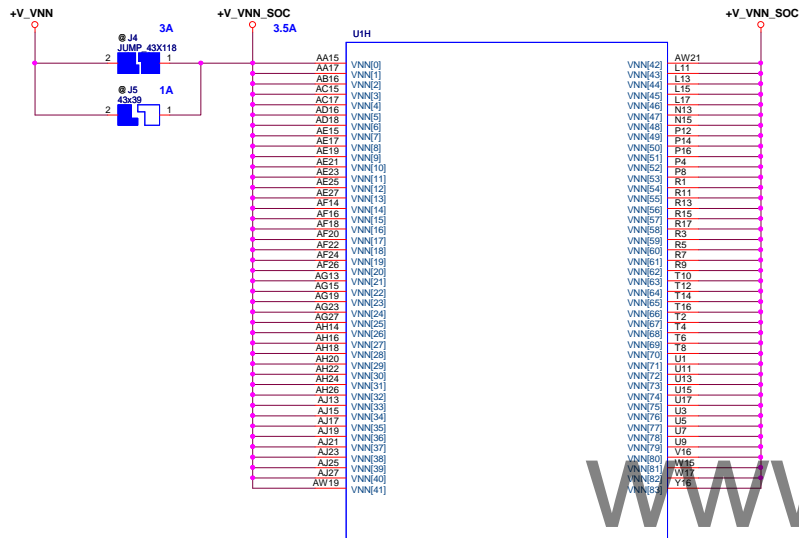


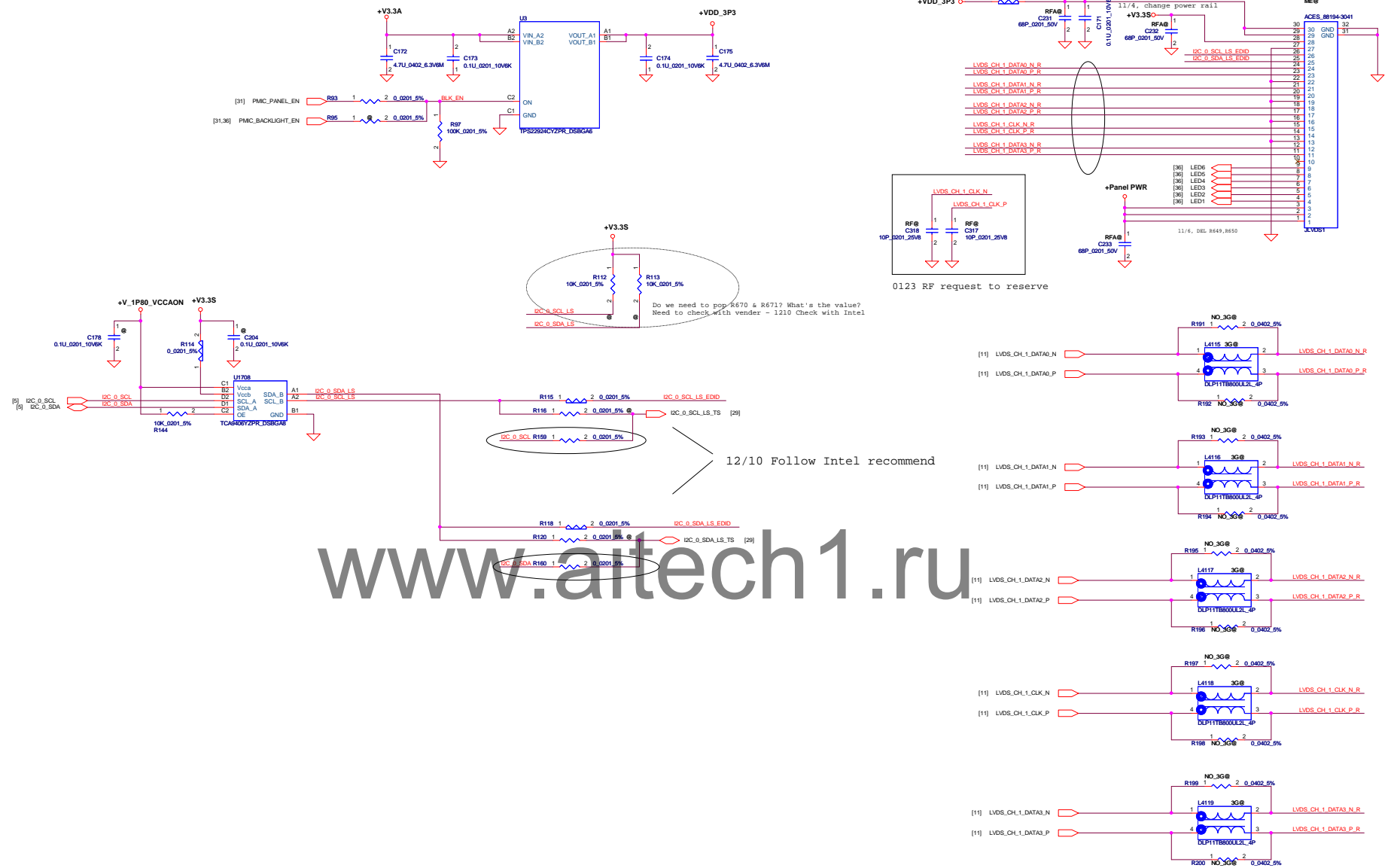
Shielding Clip



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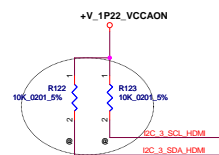
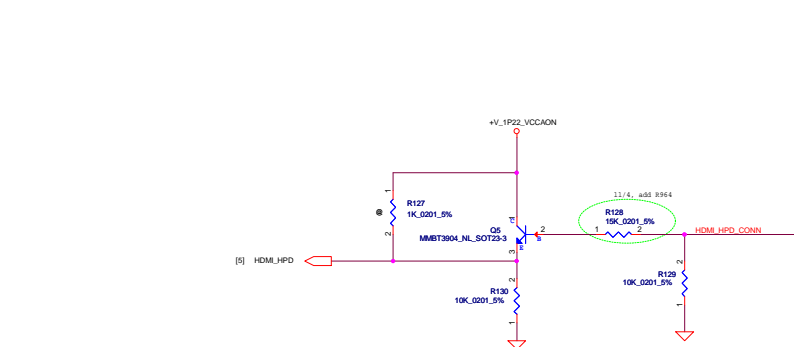




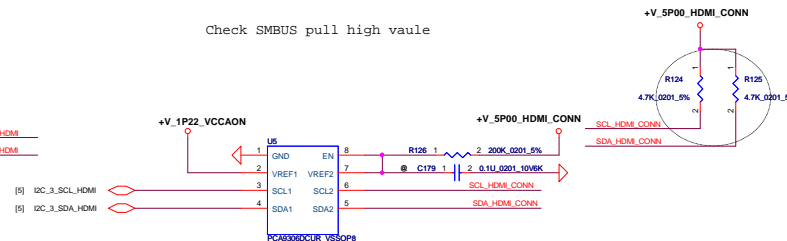


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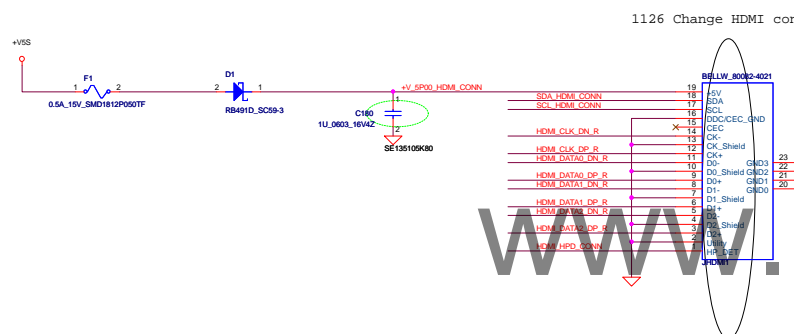
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Check SMBUS pull high vaule

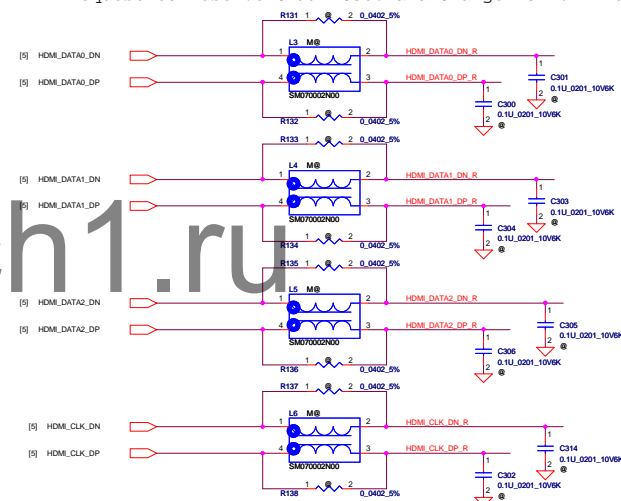


0129 Change the resistance vaule of R124 and R125.



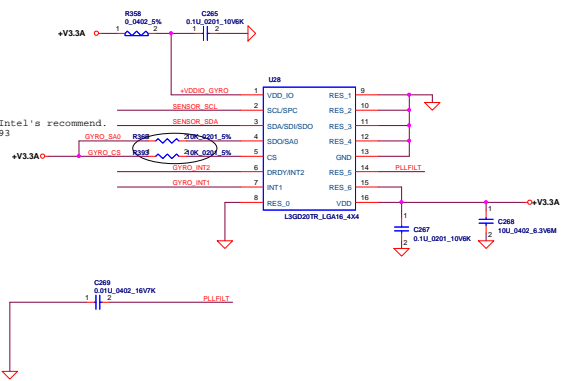
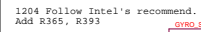
1126 Change HDMI conn into DC232001V00

0117 EMI request to reserve C299~ C306 and Change L3~L6 PN to SM070002N00

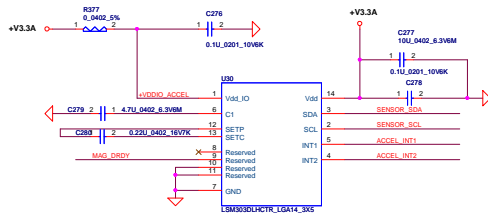


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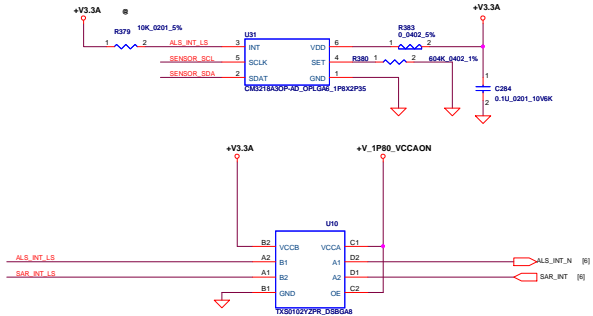
GYROSCOPE



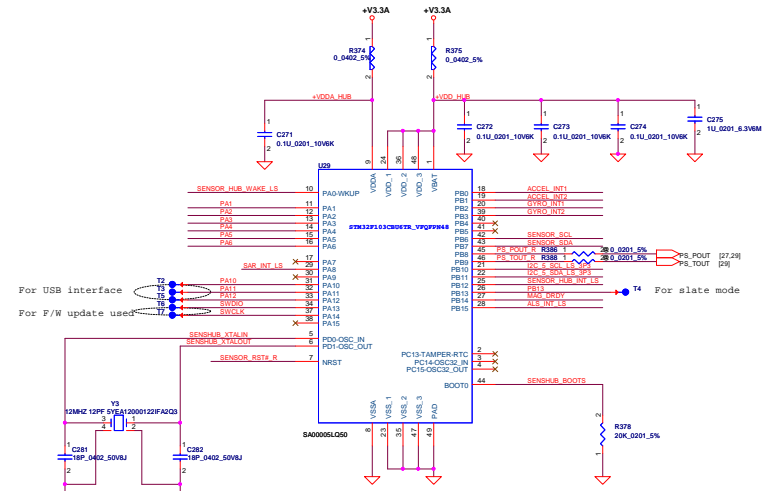
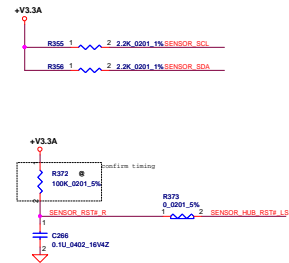
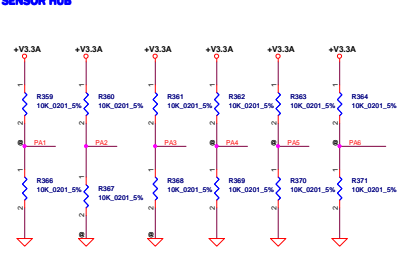
Accelerometer & E-COMPASS



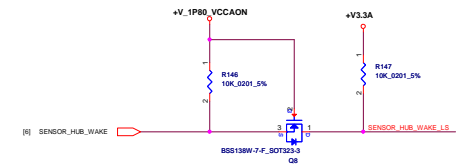
LIGHT SENSOR



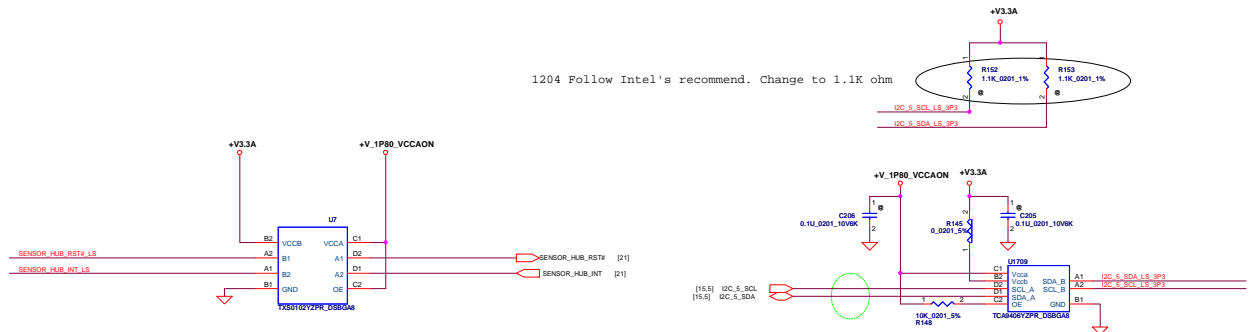
SENSOR HUB



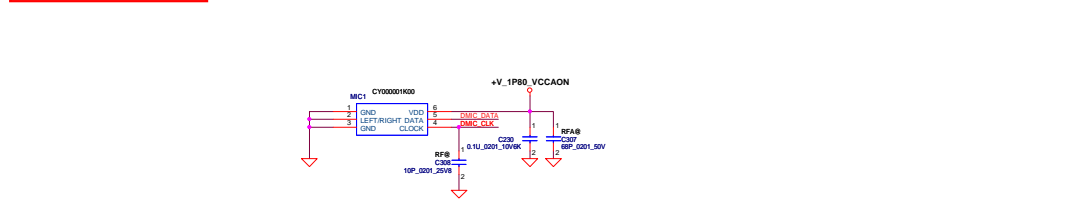
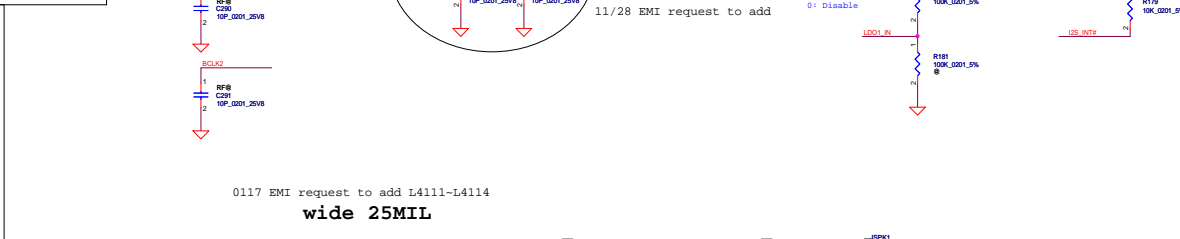
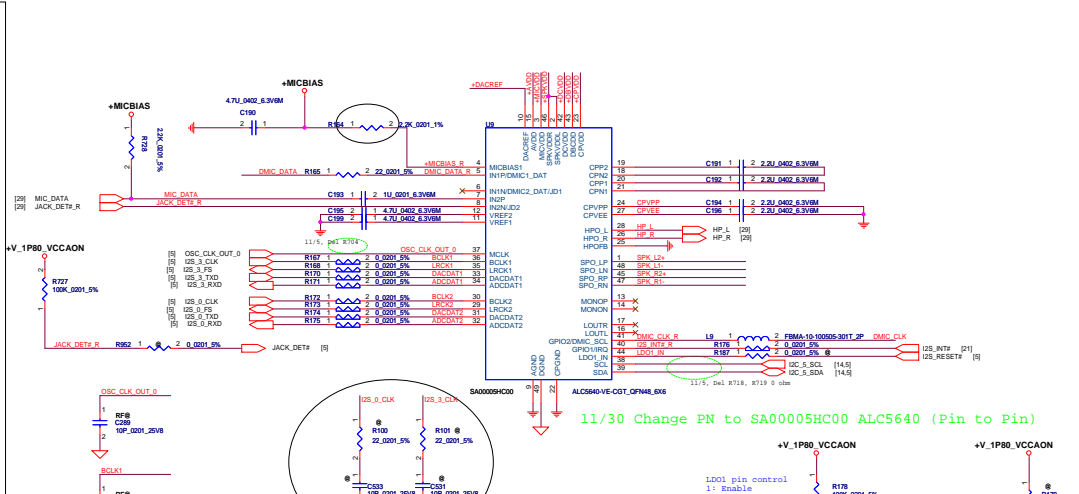
0123 According to vendor test result.
Change C281 and C282 into 18pf.



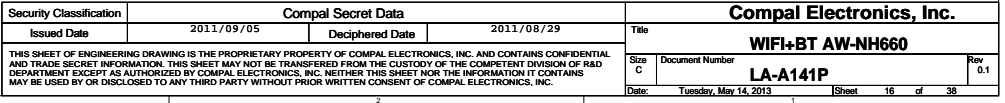
1204 Follow Intel's recommend. Change to 1.1K ohm

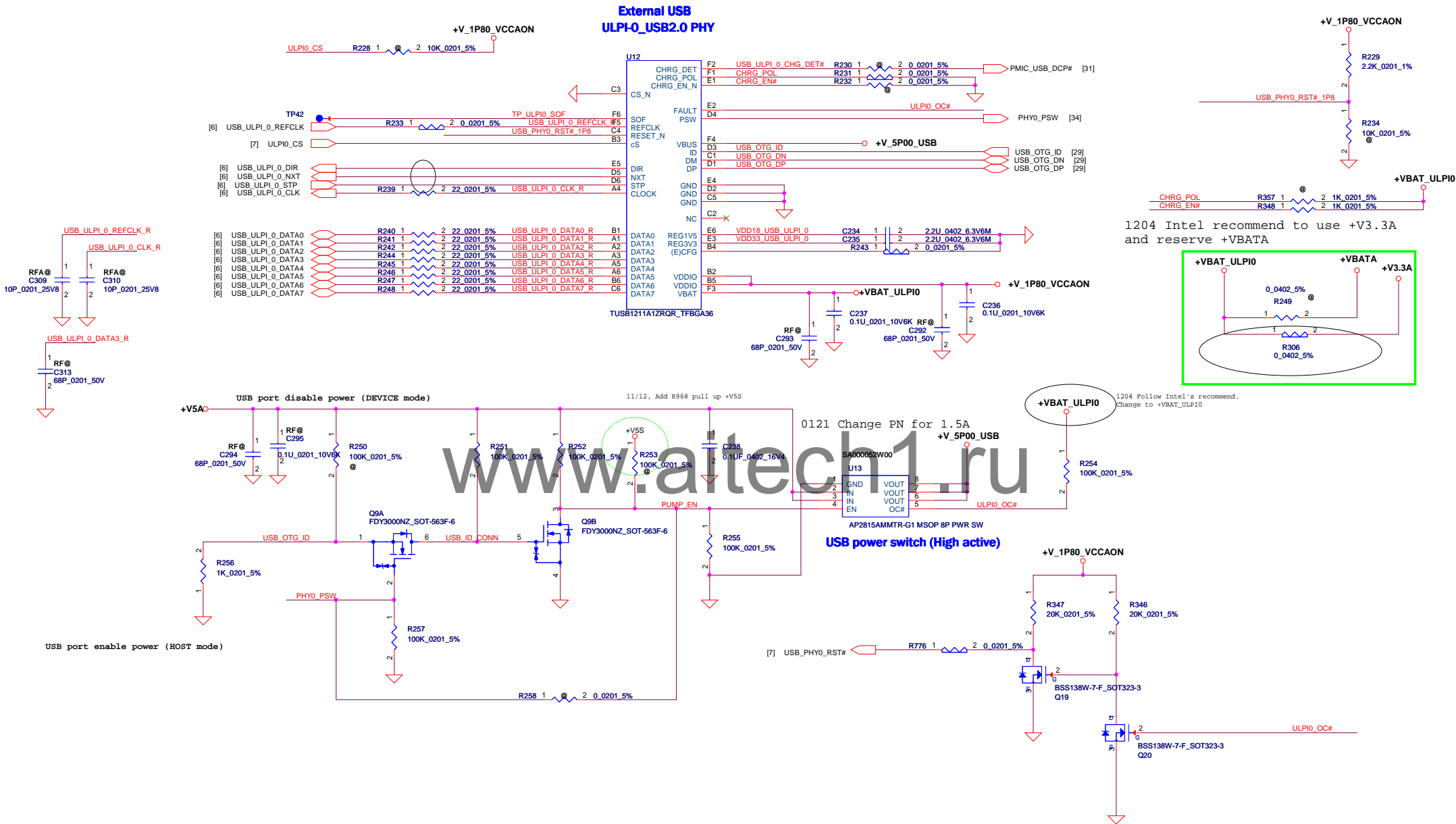


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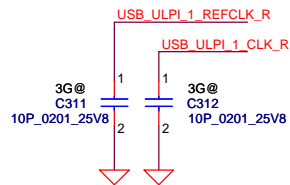


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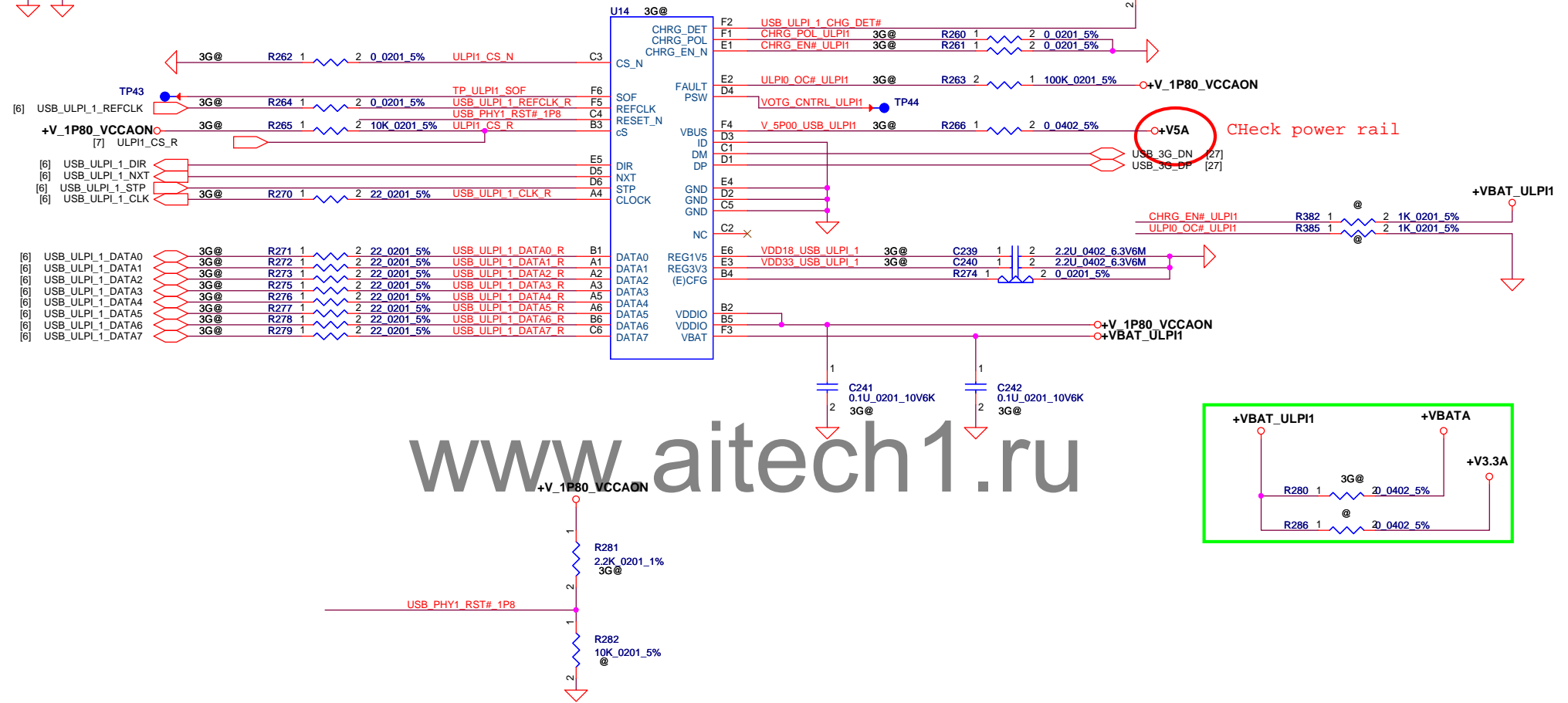




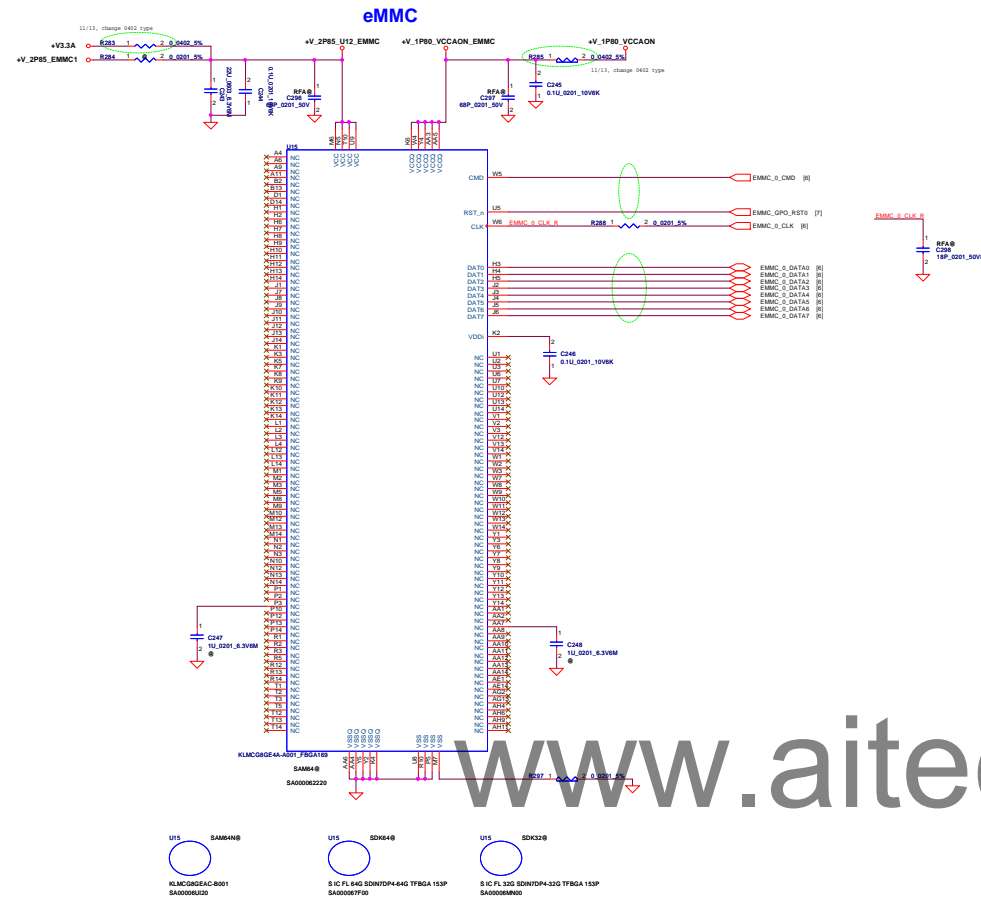
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For 3G/LTE ULPI-1_USB2.0 PHY



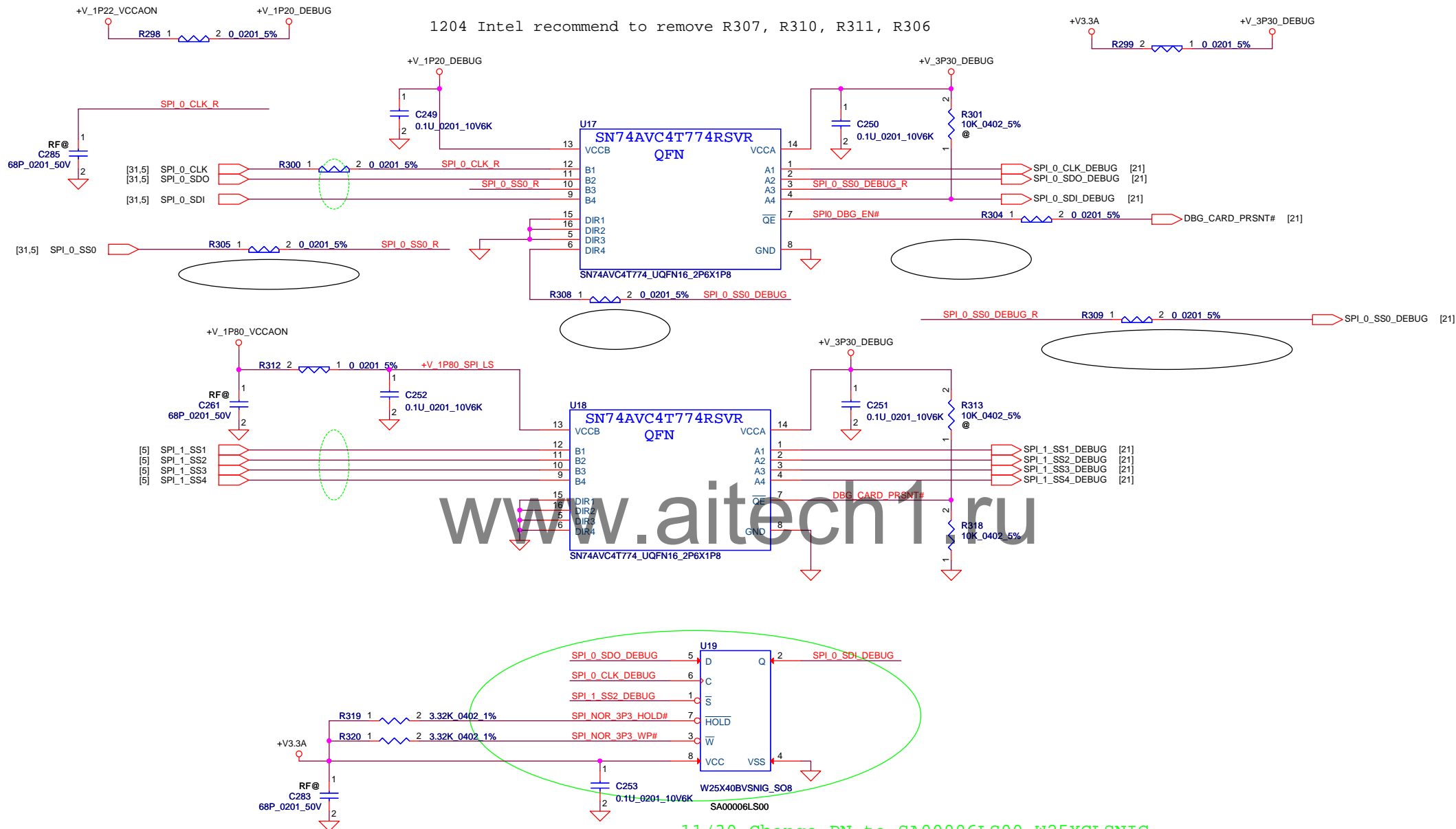
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For Sandisk footprint

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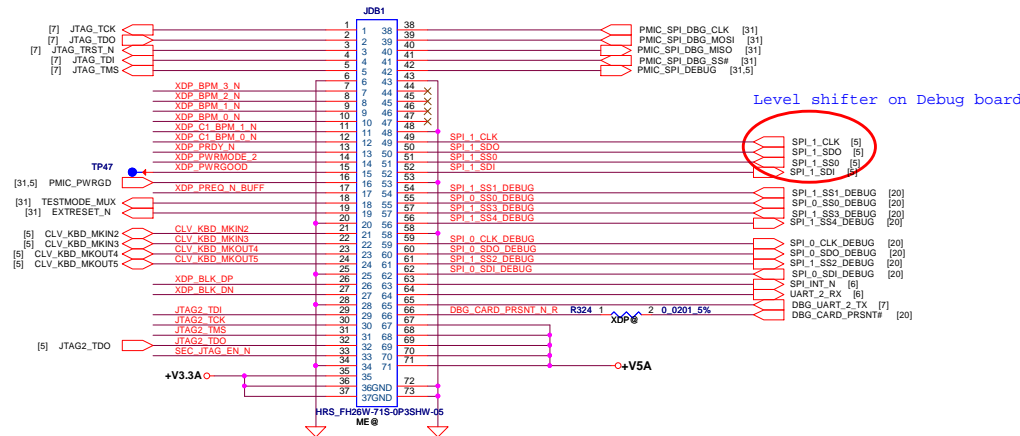
1204 Intel recommend to remove R307, R310, R311, R306



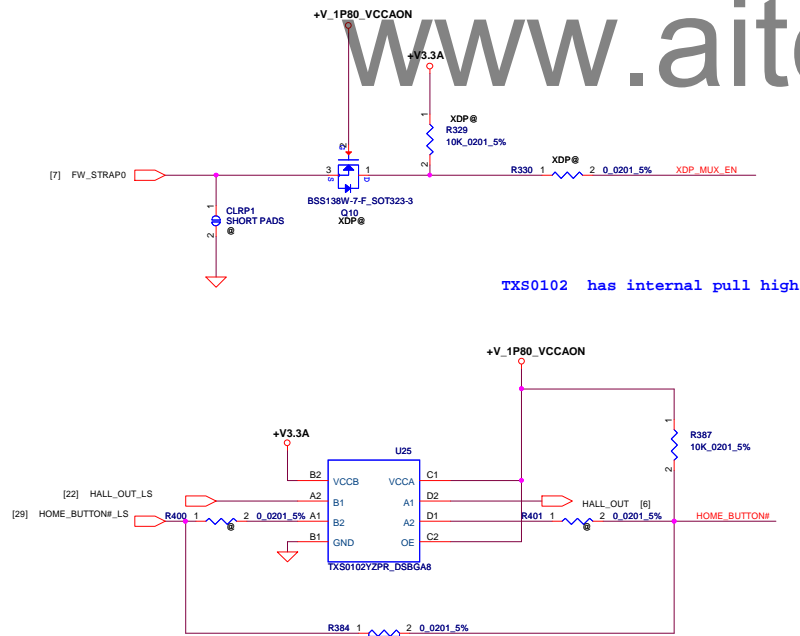
11/30 Change PN to SA00006LS00 W25XCLSNI6

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XDP

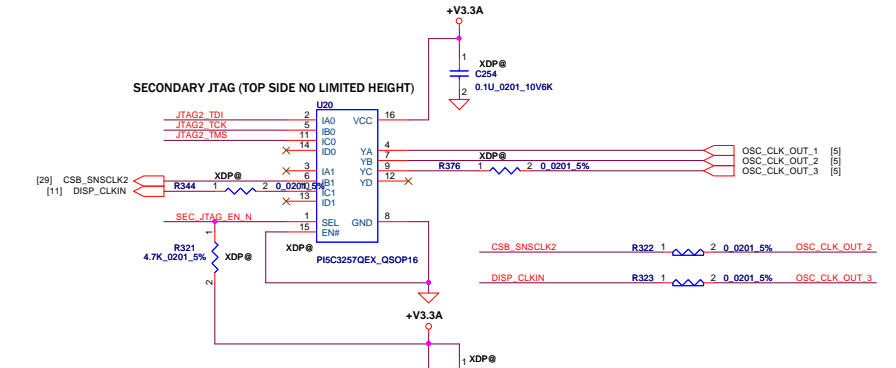


DBG_CARD_PRNT_N control circuits into debug board

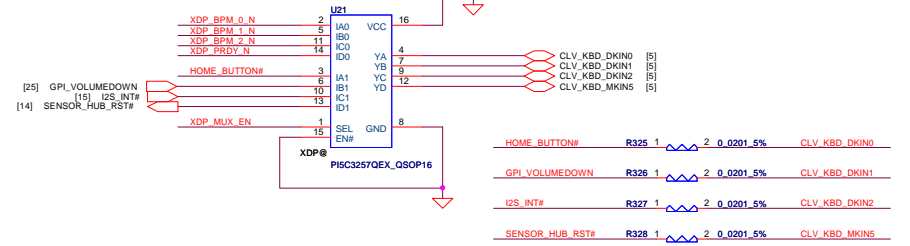


TXS0102 has internal pull high

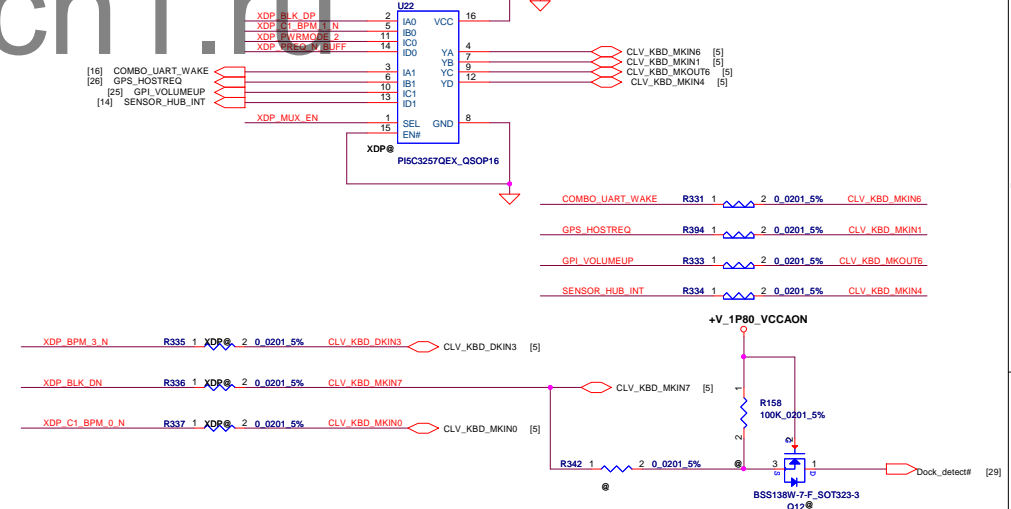
SECONDARY JTAG (TOP SIDE NO LIMITED HEIGHT)



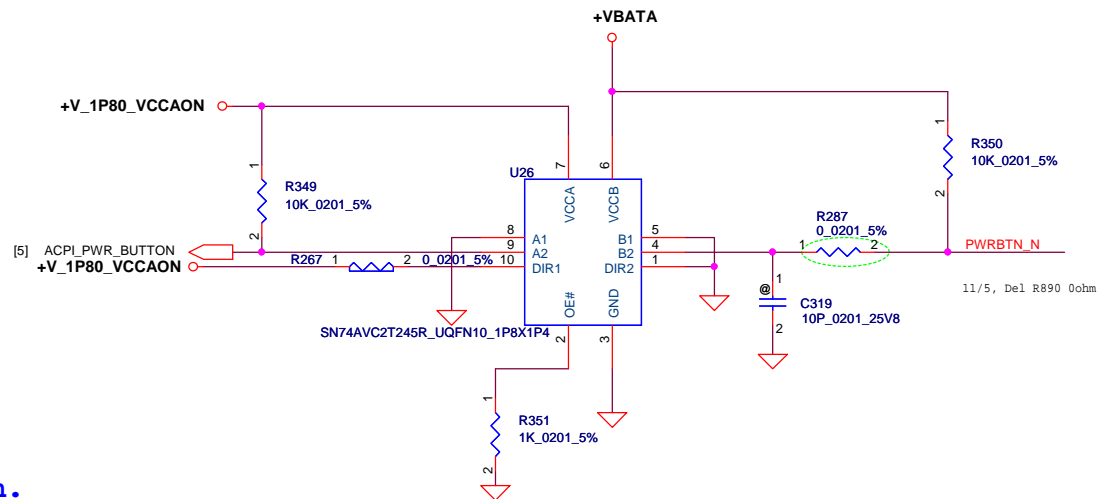
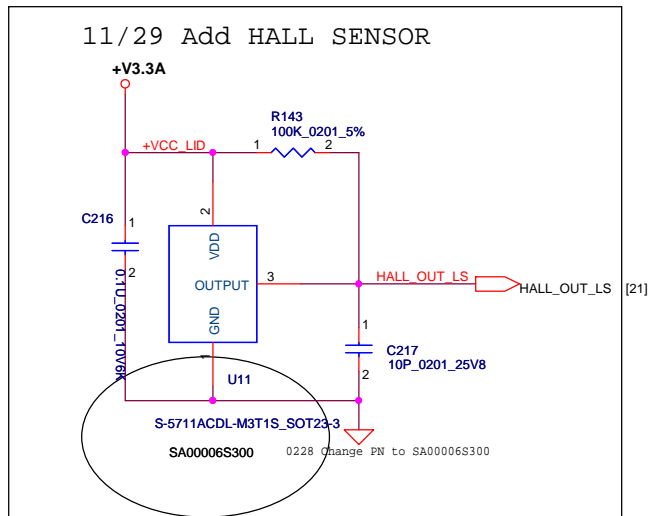
BPM SIGNALS(TOP SIDE NO LIMITED HEIGHT)



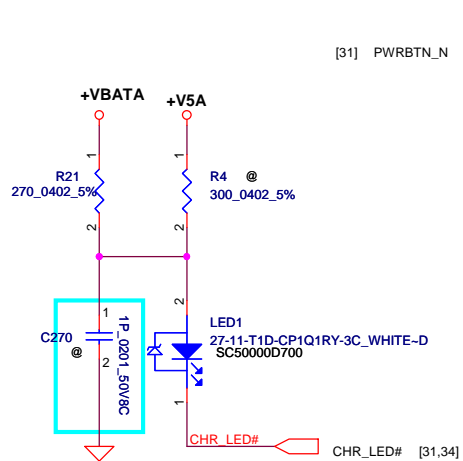
(TOP SIDE NO LIMITED HEIGHT)



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PWR Board FFC Conn.



ESD require

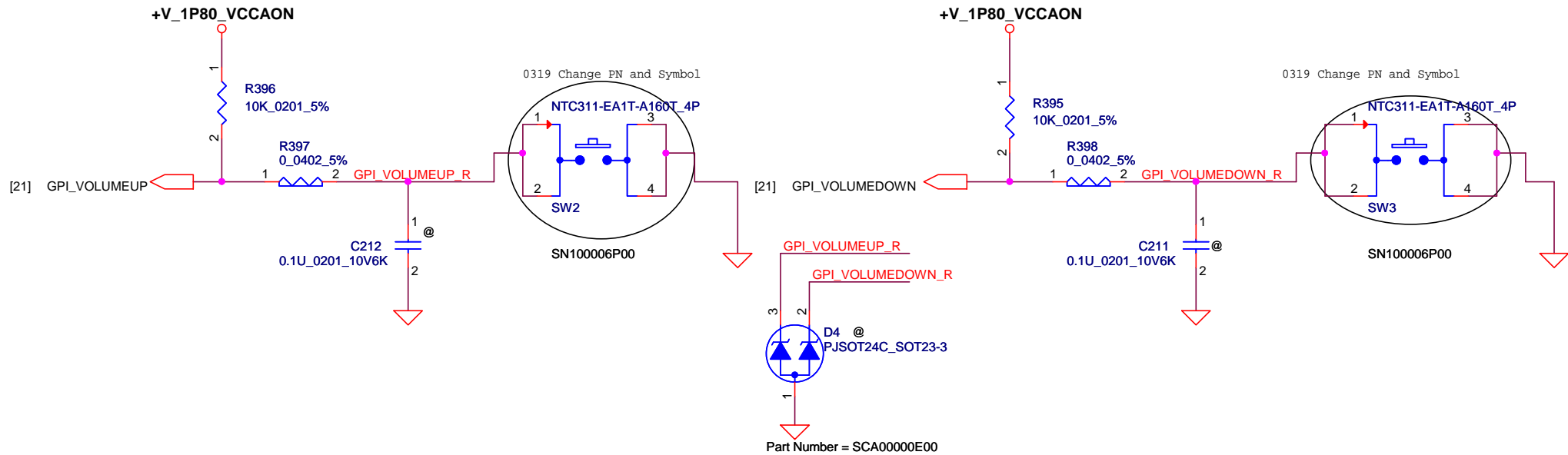
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0321 Intel recommand to reserve

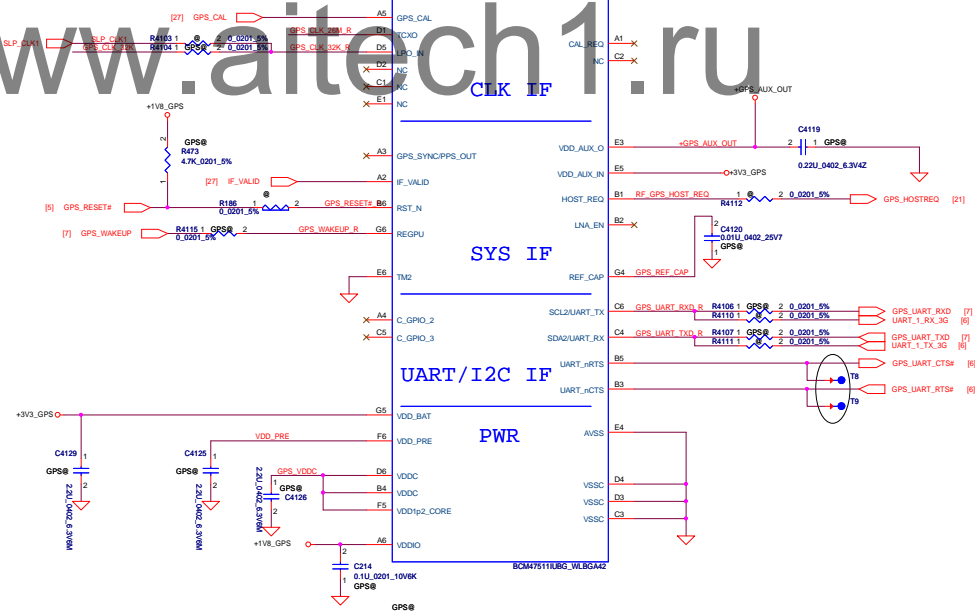
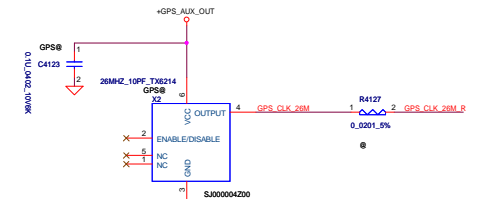
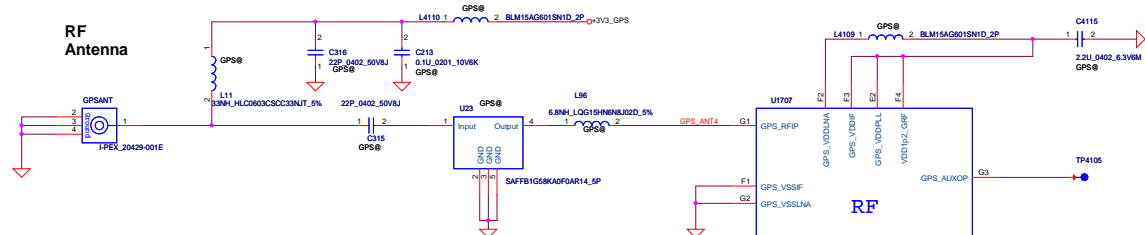
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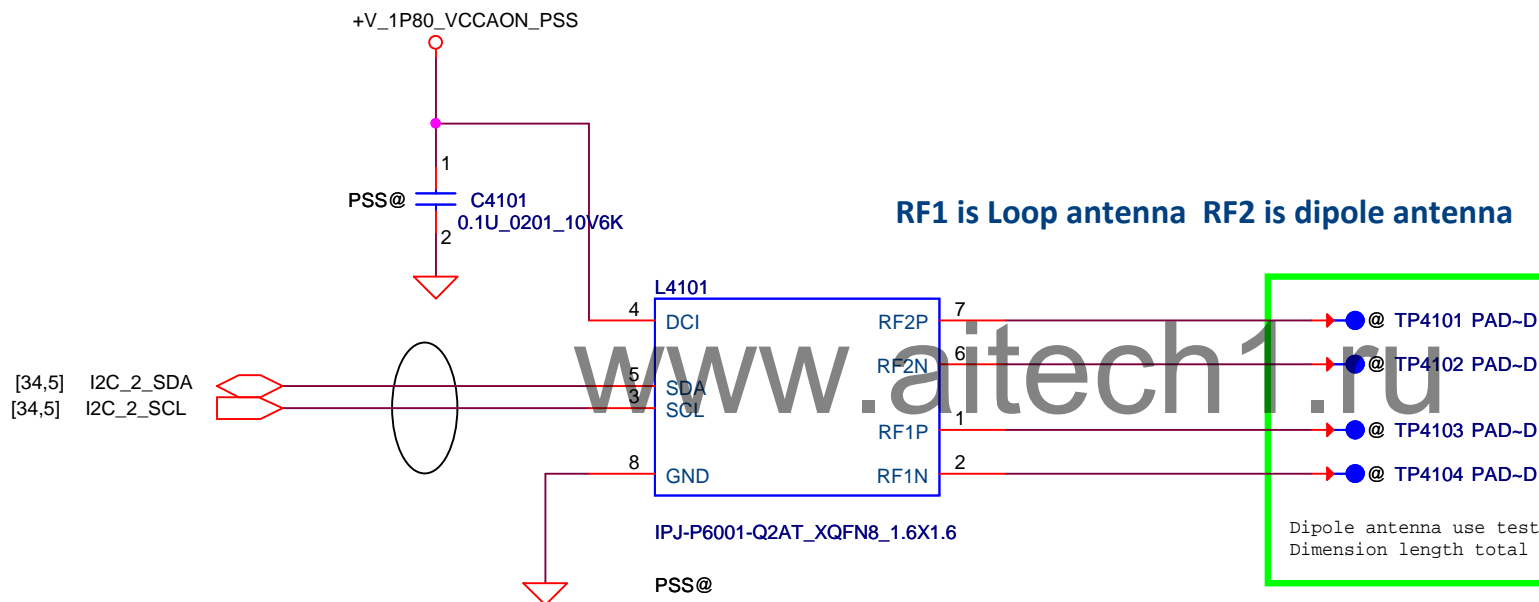
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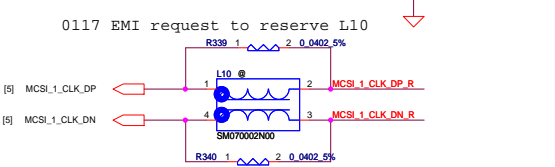
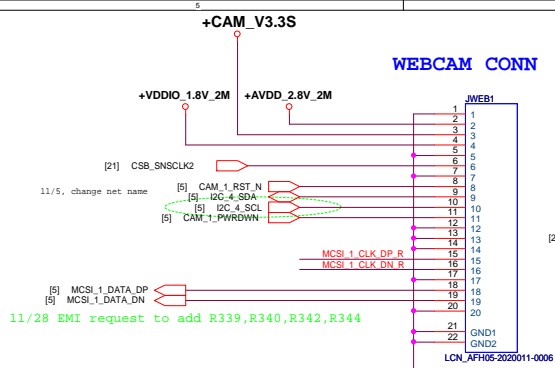
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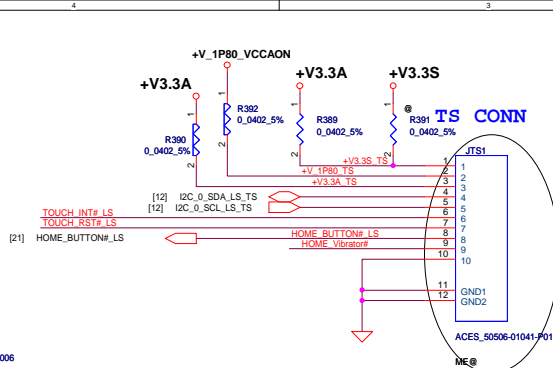
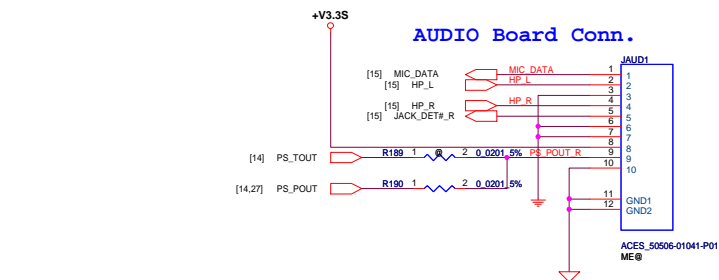
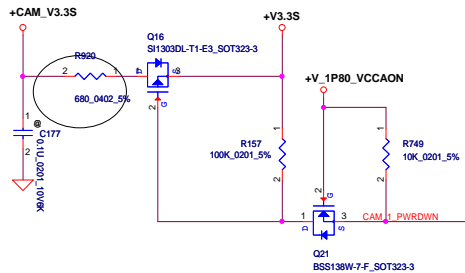


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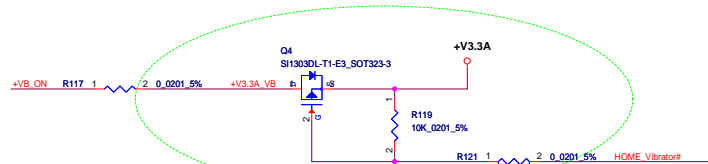
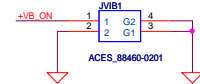


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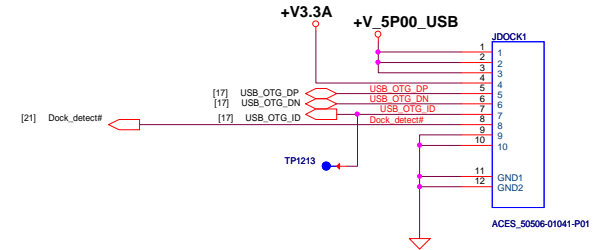
CAM vendor's recommend



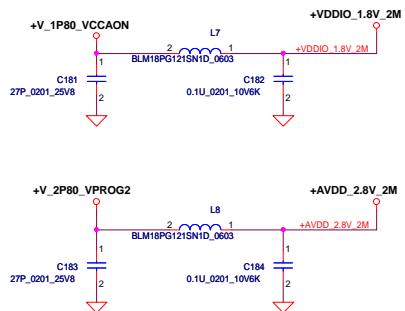
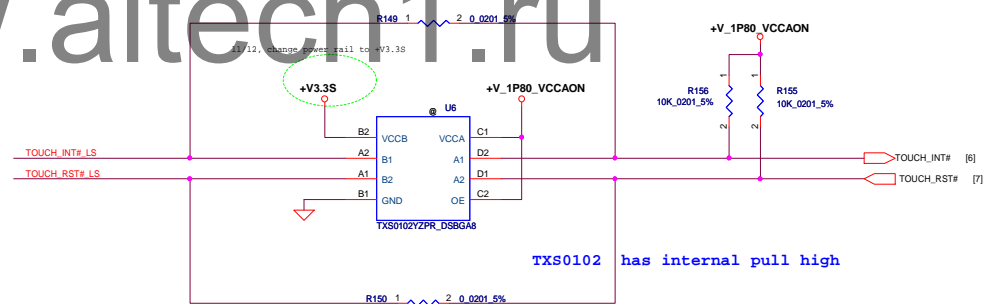
Vibrator CONN



JDOCK1 CONN



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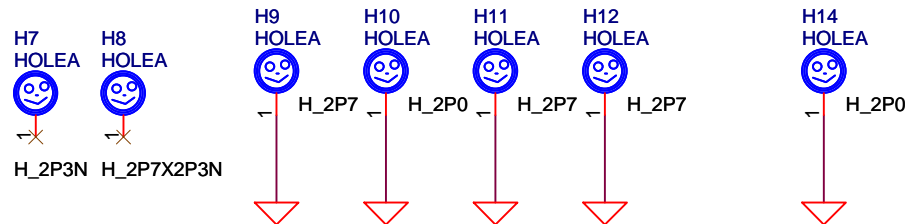
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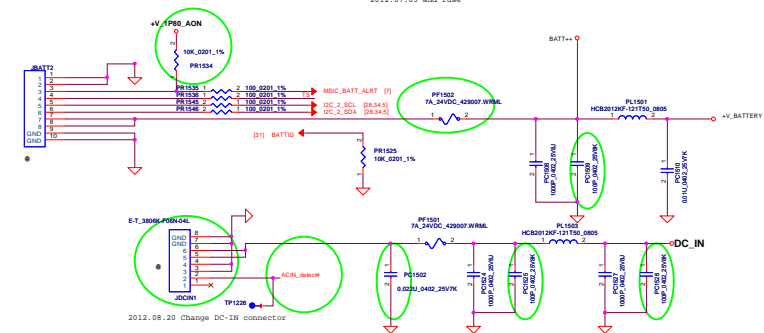


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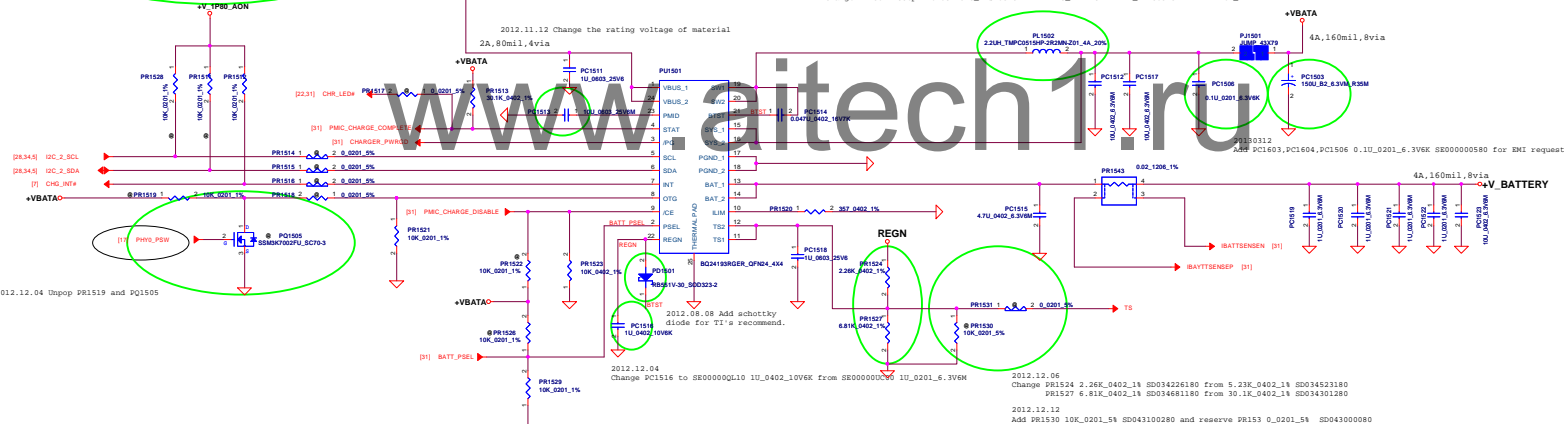
2012.07.05 add fuse

2012.08.20 Change DC-IN connector

1.11.11 add AC detect function

20130314
1.Add PC1502 to SE075223K80 0.022U_0402_25V7K for EMI

2013.01.17
Change P11502 footprint to TOKO_FDS0515-H-2R2M-P3_2P from TAI-T-TMPC0515HD-2R2MN-201_2P



2012.12.04 Unpop PR1519 and PQ1505

2012.12.04
Change PC1516 to SE000000L10 1U_0402_10V6K from SE000000UC00 1U_0201_6.3V6M

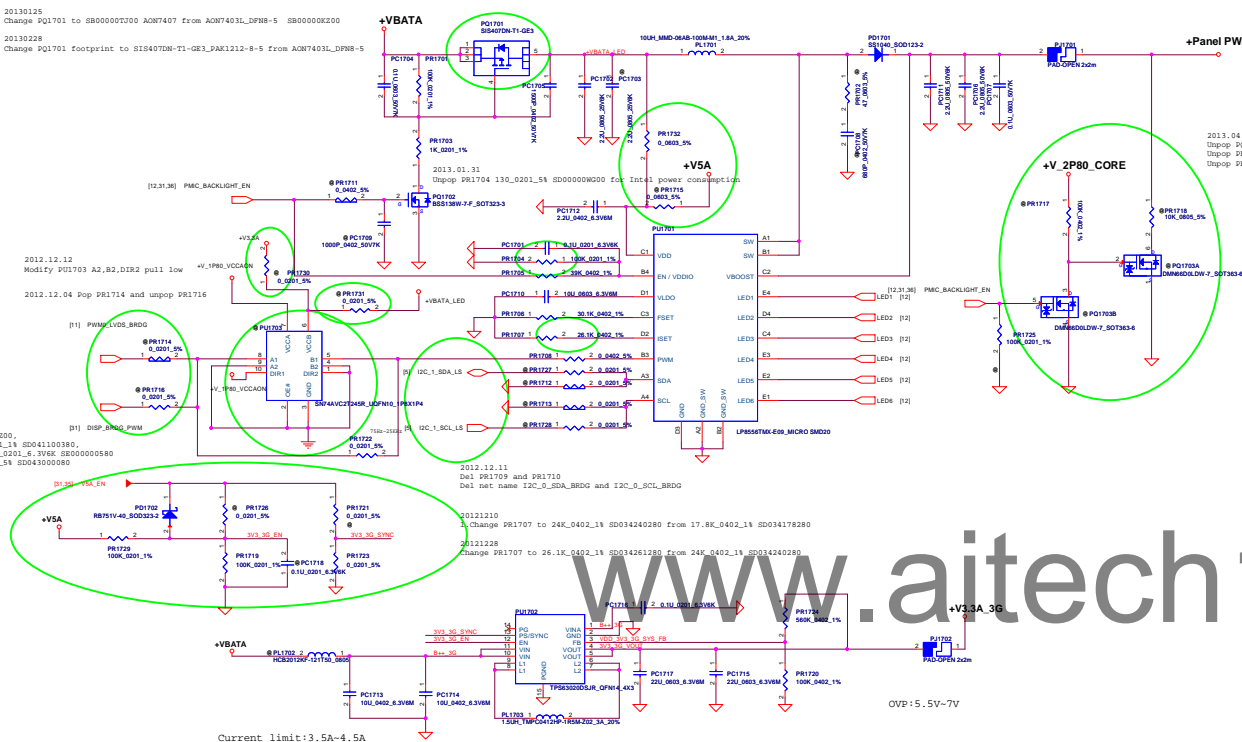
2012.12.06
Change PR1524 2.26K_0402_1# SD034226180 from 5.23K_0402_1# SD034523180
PR1527 6.81K_0402_1# SD034681180 from 30.1K_0402_1# SD034301280

```
2012.12.12
Add PR1530 10K_0201_5% SD043100280 and reserve PR153 0_0201_5% SD043000080
```

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20130125
Change PQ1701 to SB00000TJ00 AON7407 from AON7403L_DFN8-5 SB00000KZ00

20130228
Change PQ1701 footprint to SIS407DN-T1-GE3_PAK1212-8-5 from AON7403L_DFN8-5



2013.04.25
Unpop PQ1703
Unpop PR1717 100K_0402_18
Unpop PR1718 10K_0805_58

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Version change list (P.I.R. List)

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for PWR

Item	Modify List	PG#	Date	Phase
1	Add PR1249,PR1263,PU1202,PR1264,PC1203,PR1209,PQ1213,PR1265 for TI suggestion Del PQ1203,PQ1204,PR1251,PR1247,PC1210,PR1248 for TI suggestion	31	20130117	SIV
2	Add PC1230,PC1229,PC1528,PR1239 for reserve by Intel request	31	20130124	SIV
3	Depop PQ1301 and PR1302 to reduce leakage current for Intel request	32	20130124	SIV
4	Change PQ1701 to SB00000RC00 SIS407DN-T1-GE3 from AON7403L_DFN8-5 SB00000KZ00 for panel flick in low battery	36	20130125	SIV
5	Unpop PR1704 130_0201_5% SD00000WG00 for Intel power consumption	36	20130131	SIV
6	Pop PR1239 100K_0201_5% SD043100380 for Intel request	31	20130131	SIV
7	Add PQ1611, Pr1631,PR1632 and PR1629 for +V5A discharge by HW request	35	20130201	SIV
8	Add +V3.3A discharge circuit (PR1633,PR1634,PR1635 and PQ1613) for Intel request Change PQ1613B Gate net name to V5A_EN from +V5A for Intel request	35	20130304	SIT
9	1.Pop PQ1301 and PR1302 2.Change PR1302 to 300_0201_5% ohm from 470_0201_5% 3.Add PR1303 300_0201_5% 4.Change PQ1301 Gate connect to +V_2P85_EMMC1 from +V3.3A 5.Depop PC1331 for Intel request	32	20130304	SIT
10	Change PC1313 to .47U_0402_6.3V4 from 10U_0402_6.3V6M for TI request	32	20130312	SIT
11	Change PQ1202 to SB000009610(2N7002) from SB00000RM00	31	20130312	SIT
12	Change PR1265 to 0_0402_5% from 0_0201_5% for HW request	31	20130318	SIT
13	1.Add PR1636 100K_0201_5% 2.Change PQ1613 to SSM3K7002FU 3.Del PR1633 100K_0201_5% and PR1635 910_0402_5% for Intel FFRD 4.Change PQ1613B Gate net name to V5A_EN from +V5A for Intel FFRD	35	20130321	SIT
14	Change PR1520 to 357_0402_1% from 324_0402_1%	36	20130325	SIT
15	Reserve PR1730 and add PR1731 0_0201_5% for Intel request	37	20130325	SIT
16	Unpop PU1703 Pop PR1722 S RES 1/20W 0 +-5% 0201 Pop PR1704 S RES 1/20W 100K +-1% 0201 Change PR1705 to S RES 1/16W 39K +-1% 0402 from S RES 1/16W 82 +-1% 0402 for power consumption decrease	36	20130410	SIT

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Version change list (P.I.R. List)		Page 1 of 1 for EE		
Item	Modify List	PG#	Date	Phase
1	Add LED1, R4, C270 for ID changed.	P.22	2013/1/17	SIV
2	Change JP4 from 6 pins into 4 pins for exact design.	P.22	2013/1/17	SIV
3	Change SW2, SW3, JNGFF1 into DIP type for SMT request.	P.22, 27	2013/1/18	SIV
4	Change DMIC into CY000001K00 for Audio team request.	P.15	2013/1/21	SIV
5	Change R1553, R1554, R1555, R1556 into L4111, L4112, L4113, L4114 for EMI request.	P.15	2013/1/21	SIV
6	Reserve L10 for EMI request.	P.29	2013/1/22	SIV
7	Reserve C307, C308, C309, C310, C311, C312, C313 for RF request.	P.15, 17, 18	2013/1/22	SIV
8	Delete D2, D3 and Reserve C300, C301, C302, C303, C304, C305, C306, C314 for EMI request.	P.13	2013/1/23	SIV
9	Reserve R754 for Intel recommend.	P.27	2013/1/24	SIV
10	Reserve R767 for Intel recommend.	P.27	2013/1/24	SIV
11	Reserve R768 for Intel recommend.	P.27	2013/1/24	SIV
12	Change R402 power rail from +V_1P80_VCCAON to +VBATA for Intel recommend.	P.27	2013/1/24	SIV
13	Connect R779 pin1 to Q61A pin2 for Intel recommend.	P.27	2013/1/24	SIV
14	Change R124 and R125 resistance vaule to 4.7K for HDMI I2C.	P.13	2013/1/24	SIV
15	Connect R53 pin2 to ULPI1_CS_R for power consumption control.	P.18	2013/1/24	SIV
16	Connect U26 pin8 to GND for +VBATA power consumption leakage issue.	P.25	2013/1/25	SIV
17	Reserve R391 and change R389 pin1 to +V3.3A for Touch control board power sequence control.	P.29	2013/1/29	SIV
18	Add R21 to Charger LED and connect to +VBATA.	P.25	2013/3/11	SIT
19	Shift pin define of JAUD1 to add more AGND pin.	P.29	2013/3/14	SIT
20	Add R191~R200 and L4115~L4119 for RF request.	P.12	2013/3/19	SIT
21	Change Q60 into 2N7002 single NMOS.	P.27	2013/3/19	SIT
22	Add R189, R190 for PS_TOUT and PS_POUT.	P.29	2013/3/19	SIT
23	Reserve R202 and C299 for Intel recommend.	P.23	2013/3/20	SIT
24	Add L4120, R203, R204 for RF request.	P.27	2013/5/2	MP
25	Add R287, C319 for Power button de-bounce circuit.	P.22	2013/5/2	MP
26	Change Q11 into U1711 for SD card power switch.	P.23	2013/5/3	MP
27				